## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of:	)
Tadashi IGUCHI	) Group Art Unit: 2826
Application No.: 10/612,033	) Examiner: Kevin V. QUINTO
Filed: July 3, 2003	) ) )Confirmation No.: 5852
For: SEMICONDUCTOR MEMORY PREVENTING AN ELECTRIC SHORT CIRCUIT BETWEEN A WORD LINE AND A SEMICONDUCTOR SUBSTRATE, AND MANUFACTURING METHOD FOR THE SEMICONDUCTOR MEMORY (As Amended)	) ) ) ) ) ) )
Mail Ston Amendment	,

**Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## **AMENDMENT**

In reply to the Office Action mailed January 6, 2009, the period for response to which extends through April 6, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks follow the amendment section of this paper.